## Swami Ramanand Teerth Marathwada University Nanded

Choice Based Credit System (CBCS) Course Structure (New scheme)

**CLASS: B. Sc. SECOND YEAR**

**Subject: Electronics - Semester III & IV**

*(W. e. f. June 2017)*

<table>
<thead>
<tr>
<th>Semester</th>
<th>Paper No.</th>
<th>Name of the Course</th>
<th>Instruction Hrs/week</th>
<th>Total periods</th>
<th>CA</th>
<th>ESE</th>
<th>Total Marks</th>
<th>Credits</th>
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</thead>
<tbody>
<tr>
<td><strong>III</strong></td>
<td>CCEIII (Section A)</td>
<td>Amplifiers, Oscillators &amp; Multivibrators (P-VI)</td>
<td>03</td>
<td>45</td>
<td>10</td>
<td>40</td>
<td>50</td>
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<tr>
<td></td>
<td>CCE III (Section B)</td>
<td>Fundamentals of Microprocessors (P-VII)</td>
<td>03</td>
<td>45</td>
<td>10</td>
<td>40</td>
<td>50</td>
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<tr>
<td></td>
<td>CCEP II (Annual Pattern Section A)</td>
<td>P-X: Practical’s based on P-VI</td>
<td>03</td>
<td>24</td>
<td>05</td>
<td>20</td>
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<td>Practical’s based on P-VIII</td>
<td>03</td>
<td>24</td>
<td>05</td>
<td>20</td>
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<td></td>
<td>CCE S I (Section A)</td>
<td>SEC I (One SEC from any optional)</td>
<td>03</td>
<td>45</td>
<td>25</td>
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<tr>
<td><strong>IV</strong></td>
<td>CCE IV (Section A)</td>
<td>Op-Amp, It’s Applications &amp; Some Specialized ICs Theory Paper (P-VIII)</td>
<td>03</td>
<td>45</td>
<td>10</td>
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<tr>
<td></td>
<td>CCEIV (Section B)</td>
<td>Microprocessor Interfacing (P-IX)</td>
<td>03</td>
<td>45</td>
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<td>CCEP III (Annual Pattern Section A)</td>
<td>P-XI: Practical’s based on P-VII</td>
<td>03</td>
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<td>Practical’s based on P-IX</td>
<td>03</td>
<td>24</td>
<td>05</td>
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<td>25</td>
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<td>CCESII (Section B)</td>
<td>SEC II (One SEC from any optional)</td>
<td>03</td>
<td>45</td>
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<td></td>
<td>Total credits semester III and IV</td>
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</tbody>
</table>

CCE: Core course Electronics   SEC: Skill Enhancement Course   ESE: End Semester Examination   C. A.: Continuous Assessment (Internal)
Swami Ramanand Teerth Marathwada University Nanded  
Choice Based Credit System (CBCS) Course Structure (New scheme)  
B. Sc. Second Year (Semester – III)  
Semester Pattern effective from June -2017  
Electronics  
CCE III (Section A)  
*Amplifiers, Oscillators & Multivibrators (P-VI)*

<table>
<thead>
<tr>
<th>Credits: 02 (Marks: 50)</th>
<th>Periods: 45</th>
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</thead>
<tbody>
<tr>
<td><strong>Unit I: Load Lines And DC Bias Circuits</strong></td>
<td>(14 Periods)</td>
</tr>
<tr>
<td>DC Load line, Q-Point and Maximum Undistorted Output, Need for Biasing a Transistor, Factors Affecting Bias Variations, Stability factor, Beta Sensitivity, Stability Factor for CB and CE Circuits, Base Bias with Emitter Feedback, Base Bias with Collector Feedback, Base Bias with Collector and Emitter Feedback, Voltage Divider Bias, Load Line and output Characteristics, AC Load line, (Numerical Problems)</td>
<td>(Book-5)</td>
</tr>
</tbody>
</table>

**Unit II: Small Signal Amplifiers**  
11 Periods  
h-parameters, An equivalent circuit for the BJT, Transconductance Model, Analysis of CE Amplifier, CB Amplifier, CC Amplifier using h-parameters, Gain in decibels (Numerical Problems)

**Unit III: Sine Wave Oscillators**  
10 Periods  
Introduction to Positive and Negative Feedback, Requirement of an Oscillator, Barkhausen Criterion, Hartley Oscillator, Colpitt’s Oscillator, R-C Network, Phase Shift Oscillator, Wien Bridge Oscillator (Circuit diagram, Working, Expression of Frequency and Condition for Oscillations) (Numerical Problems)

**Unit IV: Multivibrators And Sweep Circuits** (10 Periods)  
Transistor as a Switch, Transistorized Astable Multivibrator, Transistorized Monostable Multivibrator, Transistorized Bistable Multivibrator (working and waveforms), Introduction to Sweep Circuits, Sweep Voltage Waveforms, Exponential Sweep, RC Ramp Generator, (Numerical Problems)

**References:**

1. Introduction To Electronics  
2. Solid State Pulse Circuits  
   - David A. Bell (4/e, Prentice-Hall of India Private Ltd.)
3. Electronic Fundamentals And Applications  
   - John D. Ryder (Prentice-Hall of India Private Ltd.)
4. Electronics And Radio Engineering  
   - M.L.Gupta (Dhanpat Rai and Sons)
5. Basic Electronics (Solid State) [Multicolour Illustrative Edition]  
   - B. L. Theraja (S. Chand & Company Ltd)
6. Electronic Principles  
7. Principles of Electronics (Vol. II)  
   - B.V. Narayana Rao (Second Edition) Published by New Age International (P) Ltd.
Unit I: Introduction To Microprocessor Intel 8085
Semiconductor Memories (RAM, ROM, PROM, EPROM, EEPROM), Block Diagram of Microcomputer (Microprocessor Based System), Block Diagram of Intel 8085, Function of Each Block, Functional Pin Diagram of Intel 8085, Features of Intel 8085

Unit II: Instruction Set Of Intel 8085
Instruction Format (1 byte, 2 byte, 3 byte), Addressing Modes, Classification of Instructions, Instruction Set of 8085

Unit III: Programming And Interrupts of 8085
Simple Programs Based on Data Transfer, Arithmetic, Logical, Branching and Machine Control Instructions, Interrupts:-Hardware Interrupts, Software Interrupts, Priority Structure of 8085 Interrupts

Unit IV: Introduction To Microprocessor Intel 8086
Block Diagram of Intel 8086, Function of Each Block, Functional Pin Diagram of Intel 8086, Features of Intel 8086

References:
1. Fundamentals Of Microprocessors And Microcomputers
   -B. Ram (6/e, Dhanpat Rai, Publications)
2. Microprocessor
   -Borole and Vibhute (2/e, Technova Publications)
3. Microprocessor Architecture, Programming And Applications With The 8085
   -Ramesh S. Gaonkar (3/e, Penram International Publishing)
4. 8085 Assembly Language Programming
   -Lance A. Leventhal (McGraw Hill International Editions)
5. Advanced Microprocessor
   -Ajay K. Ray & Kishor M. Bhurchandi (TMH Publication, 7th Revised Edition)
6. Microprocessors & Interfacing
Swami Ramanand Teerth Marathwada University Nanded

Choice Based Credit System (CBCS) Course Structure (New scheme)

B. Sc. Second Year (Semester – IV)

Semester Pattern effective from June -2017

Electronics

CCE IV (Section A)

Op-Amp, It’s Applications & Some specialized ICs (P-VIII)

<table>
<thead>
<tr>
<th>Credits: 02 (Marks: 50)</th>
<th>Periods: 45</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Unit I: Operational Amplifier</strong></td>
<td>(13 Periods)</td>
</tr>
<tr>
<td>Theory of Differential Amplifier, Block Diagram of Op-Amp, Schematic Symbol, Ideal Characteristics, Input Offset Voltage, Input Offset Current, Input Bias Current, Input Impedance, Output Impedance, Open Loop Gain, CMRR, Slew Rate, Numerical Problems</td>
<td></td>
</tr>
</tbody>
</table>

| **Unit II: Applications of Operational Amplifier** | (13 Periods) |

| **Unit III: Active Filters** | (13 Periods) |

| **Unit IV: Specialized ICs** | (06 Periods) |
| Block Diagram of IC555, IC 555 as Astable Multivibrator, IC555 as Monostable Multivibrator, IC566 (Pin Diagram, Block Diagram and Use as VCO), Numerical Problems |

**References:**

1. Op-Amps And Linear Integrated Circuits
   - Ramakant Gayakwad (Prentice Hall of India Private Limited)
2. Electronic Fundamentals And Applications
   - John D. Ryder (Prentice Hall of India Private Limited)
3. Electronic Principles
   - A. P. Malvino (TMH Publishing Company)
4. Electronics and Radio Engineering
   - M.L.Gupta (Dhanpat Rai and Sons)
Swami Ramanand Teerth Marathwada University Nanded

Choice Based Credit System (CBCS) Course Structure (New scheme)
B. Sc. Second Year (Semester – IV)
Semester Pattern effective from June -2017

Electronics
CCE IV (Section B)
Microprocessor Interfacing (P-IX)

Courses: 02 (Marks: 50)

<table>
<thead>
<tr>
<th>Unit I: Basic Interfacing Concepts</th>
<th>Periods: 45</th>
</tr>
</thead>
<tbody>
<tr>
<td>(10 Periods)</td>
<td></td>
</tr>
<tr>
<td>Introduction, memory mapped I/O scheme, I/O mapped I/O scheme, Data Transfer Schemes:- Synchronous, Asynchronous, Interrupt Driven and DMA</td>
<td></td>
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</tbody>
</table>

<table>
<thead>
<tr>
<th>Unit II: Interfacing Chips</th>
<th>Periods: 45</th>
</tr>
</thead>
<tbody>
<tr>
<td>(15 Periods)</td>
<td></td>
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<tr>
<td>Schematic Diagram (Functional Pin Diagram), Block diagram and Operating modes of the ICs- 8253, 8255, 8259, 8257, Control registers of 8255 and 8253</td>
<td></td>
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</tbody>
</table>

<table>
<thead>
<tr>
<th>Unit III: Microprocessor Applications</th>
<th>Periods: 45</th>
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<tbody>
<tr>
<td>(10 Periods)</td>
<td></td>
</tr>
<tr>
<td>Demultiplexing of AD7–AD0 bus, Interfacing concepts of I/O devices using decoder (74LS138), Chip Select logic, Generation of MEMR, MEMW, IOR and IOW signals, Tristate buffer (74LS244), Latches (74LS373), Interfacing switches, LED, relays</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Unit IV: Data Converters</th>
<th>Periods: 45</th>
</tr>
</thead>
<tbody>
<tr>
<td>(10 Periods)</td>
<td></td>
</tr>
<tr>
<td>Interfacing of ADC 0808 &amp; DAC 0808 using 8255</td>
<td></td>
</tr>
</tbody>
</table>

References:
1. Fundamentals Of Microprocessors and Microcomputers:
   - B. Ram (Dhanpat Rai Publications)
2. Microprocessor Architecture, Programming And Applications With 8085:
   - Ramesh S. Gaonker (3/e, Penram International Publishing)
3. Introduction to 8085, 8086 Microprocessors And Peripherals
4. Microprocessor:
   - Borole and Vibhute (2/e, Technova Publications)
Swami Ramanand Teerth Marathwada University Nanded
Choice Based Credit System (CBCS) Course Structure (New scheme)
B. Sc. Second Year (Semester - III & IV)
Semester Pattern effective from June -2017
Electronics
Practical Paper P-X: Practical’s based on P-VI & P-VIII
(Annual practical’s Based on [CCE III& IV (Section A & B)])

Credits: 02 (Marks: 50)  Periods : 45

Note:
1. Every student must perform at least TEN experiments (At least FIVE from each group)
2. Use graphs wherever necessary

List of Experiments:

Group I:
1. Op-Amp as Inverting Amplifier (DC Gain Verification)
2. Op-Amp as Non-inverting Amplifier (DC Gain Verification)
3. Op-Amp as Inverting Amplifier (Study of Frequency Response, Gain & -3db Band Width)
4. Op-Amp as Non-inverting Amplifier (Study of Frequency Response, Gain & -3db Band Width)
5. Op-Amp as Adder
6. Op-Amp as Subtractor
7. Op-Amp as Integrator
8. Op-Amp as Schmitt’s Trigger
9. Op-Amp as Comparator
10. Op-amp as Analog Computer
11. IC555 Timer as Astable Multivibrator (Measurement of Pulse Width, Space Width, Time Period, Frequency and Mark to Space Ratio)
12. VCO using IC566 (Measurement of Frequency with Change in Control Voltage)

Group II:
13. Study of Transistorized CE Amplifier (Frequency Response, Gain & -3db Band Width)
14. Transtorized Hartely oscillator (Measurement of Frequency and Amplitude of Waveforms)
15. Transtorized Colpitt’s Oscillator (Measurement of Frequency and Amplitude of Waveforms)
16. Transtorized Phase Shift Oscillator (Measurement of Frequency and Amplitude of Waveforms)
17. Wein Bridge Oscillator using Op-Amp (Measurement of Frequency and Amplitude of Waveforms)
18. Transistorized Astable Multivibrator. (Measurement of Pulse Width, Space Width, Time Period, Frequency and Duty Cycle)
19. Transistorized Mono stable multivibrator (Measurement of Gate Width)
20. Transistorized Bistable Multivibrator
21. RC Ramp Generator using Transistor. (Measurement of Rise Time, Fall Time and Frequency)
Paper-XI
LAB-III
(Practical Based On Papers VII And IX) (50 Marks)

Note:
1. Every student must perform at least 10 experiments.
2. Use flow-chart wherever necessary.

List of Experiments:
1. ALP to Transfer a block of data from one location to another location
2. ALP for addition of two byte and result 8-bit
3. ALP for addition of two byte and result 16-bit numbers
4. ALP for subtraction of two bytes
5. ALP for decimal addition of 8 bit numbers
6. ALP for 1’s complement of 8-bit and 16-bit numbers
7. ALP to find 2’s complement of 8-bit and 16-bit numbers
8. ALP for shifting of 8-bit number:
   a. Left by one bit position
   b. Left by two bit position
9. ALP to find sum of series of 8-bit numbers
10. ALP to find multiplication of two 8-bit numbers
11. ALP to find division of two 8-bit numbers
12. ALP for masking off:
   a. Four LSBs of 8-bit numbers
   b. Four MSBs of 8-bit numbers
13. ALP to find smallest number of the series
14. ALP to find largest number of the series
15. ALP to generate square wave using IC 8255. Determine frequency
16. Interfacing of 7-segment display with 8085 using IC 8255
Swami Ramanand Teerth Marathwada University Nanded
Choice Based Credit System (CBCS) Course Structure (New scheme)
B. Sc. Second Year (Semester - IV)
Semester Pattern effective from June - 2017
Electronics
CCESI (Section A )
Skill Enhancement Course SEC I

<table>
<thead>
<tr>
<th>Skill Enhancement Course-I (Credit:2) B. Sc. III SEM</th>
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<tbody>
<tr>
<td>Physics Workshop Skill</td>
</tr>
<tr>
<td>Credit: 02</td>
</tr>
<tr>
<td>45 Lectures ( Theory + Lab)</td>
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</tbody>
</table>

Maintain project file or Dissertation to check Analytic skills/ problem solving in skill exam

UNIT-I: Mesasurement Skill : Measureing units, conversion to SI and CGS. Familiarization with meter scale, vernier caliper, Screw gauge and their utility. Measure the dimension of solid bulk, volume of cylindrical beaker / glass, diameter of thin wire, thickness of metal sheet etc. (15 Lectures)


Hands on Exercises:
(15 Lectures)
1. Measurement of ac and dc voltages/ currents by using analogue multimeter
2. Measurement of ac and dc voltages / currents by using digital multimeter
3. Testing of electronic components by using multimeter such as diodes, transistors FETs etc.
4. Measurement of voltage, time period and frequency using CRO.
5. Measurement of rise and fall time using CRO.
6. Study wave forms generated by a function generator.

Reference Books:
OR
Swami Ramanand Teerth Marathwada University Nanded
Choice Based Credit System (CBCS) Course Structure (New scheme)
B. Sc. Second Year (Semester – IV)
Semester Pattern effective from June -2017
Electronics
CCES II (Section B)
Skill Enhancement Course SEC II

Credits: 02 (Marks: 50) Periods: 45

<table>
<thead>
<tr>
<th>Skill Enhancement Course-I (Credit: 02) B. Sc. III SEM</th>
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<tbody>
<tr>
<td>Optics and Lasers</td>
<td>Maximum Marks: 50</td>
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<tr>
<td>Credit: 02</td>
<td>C. A. (Internal): 25</td>
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<tr>
<td>45 Lectures (Theory + Lab)</td>
<td>ESE OR Skill Exam:25</td>
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<tr>
<td>Maintain project file or Dissertation to check Analytic skills/ problem solving in skill exam</td>
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UNIT-II: Refraction Through Lenses: Types of lenses, The sign convention, principal foci, Deviation produced by a thin lens, Power of a lens, Principal planes and focal planes, Dispersion by prism, Dispersive power, Huygens eyepiece, Ramsden eyepiece. (10 Lectures)


Hands on Exercises: (15 Lectures)
1. Determination of focal length of a biconvex lens.
2. Determination of radius of curvature of a lens using a spherometer.
3. Determination of power of a lens.
4. Determination of the grating radial spacing of a compact disc (CD) by reflection using a laser source.
5. To find the width of the slit using diffraction pattern obtained by a laser.
6. To find angle of polarization using Brewster law.
7. Study the characteristics of solid state laser.
8. Study the characteristics of LDR.
Reference Books:

4. Laser & Non- linear optics, B. B. Laud, New Age International Publisher
Swami Ramanand Teerth Marathwada University Nanded

Choice Based Credit System (CBCS) Course Structure (New scheme)

B. Sc. Second Year (Semester – IV)

Semester Pattern effective from June -2017

Electronics

CCESI (Section A)

Skill Enhancement Course SEC II

<table>
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<th>Skill Enhancement Course-II (Credit:2) B. Sc. IV SEM</th>
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<td>45 Lectures (Theory + Lab)</td>
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<td>Maintain project file or Dissertation to check Analytic skills/ problem solving in skill exam</td>
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Skill Enhancement Course-II (Credit:2) B. Sc. IV SEM

<table>
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<tr>
<th>Credit: 02</th>
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<tbody>
<tr>
<td>45 Lectures (Theory + Lab)</td>
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</table>

Maximum Marks: 50
C. A. (Internal): 25
ESE OR Skill Exam: 25


(20 Lectures)

**UNIT-II: Solid state Devices**: Resistors, inductors and capacitors. Diode and rectifiers. Components in series or in shunt. Response of inductors and capacitors with DC or AC sources.

(10 Lectures)

**Hands on Exercises**:

(15 Lectures)

1. To Study charging and discharging of a condenser through resistor R.
3. Study of transducers.(Thermistor, LDR, Photodiode photo transistor etc.)
4. Study frequency response of a microphone.
5. Soldering Skills.
7. Design and development of low voltage power supply.
9. Study of response of inductors and capacitors with DC or AC sources.
10. Study of capacitance by using LCR meter.

**Reference Books**:

3. A text book of Electrical Technology – A K Theraja
**Skill Enhancement Course-II (Credit: 02) B. Sc. IV SEM**

<table>
<thead>
<tr>
<th>Renewable Energy &amp; Energy Harvesting</th>
<th>Maximum Marks: 50</th>
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<tr>
<td>Credit: 02</td>
<td>C. A. (Internal): 25</td>
</tr>
<tr>
<td>45 Lectures (Theory + Lab)</td>
<td>ESE OR Skill Exam: 25</td>
</tr>
</tbody>
</table>

Maintain project file or Dissertation to check Analytic skills/ problem solving in skill exam

**UNIT-I: Solar Energy** : Solar Energy, its importance, storage of solar energy, solar pond, non convective solar pond, applications of solar pond and solar energy, solar water heater, flat plate collector, solar distillation, solar cooker, solar green houses, solar cell, absorption air conditioning. Need and characteristics of photovoltaic (PV) systems, PV models and equivalent circuits, and sun tracking systems.  

(15 Lectures)

**UNIT-II: Piezoelectric Energy harvesting** : Introduction, physics and characteristics of piezoelectric effect, materials and mathematical description of piezoelectricity, Piezoelectric parameters and modeling piezoelectric generators, piezoelectric energy harvesting applications, Human power.  

(15 Lectures)

**Hands on Exercises** : (15 Lectures)  
Demonstrations and Experiments  
1. Demonstration of Training modules on Solar Energy, wind energy, etc.  
2. Conversion of vibration to voltage using piezoelectric materials.  
3. Conversion of thermal energy into voltage using thermoelectric modules.  
4. Teacher can allot any exercise related with topic.

**Reference Books:**  
6. J. Balfour, M. Shaw and S. Jarosek, Photovoltaics, Lawrence J Goodrich (USA)  
IV Semester : model paper. Communication and soft skills (css-iii)- Common for All UG Courses. (w.e.f Admitted batch 2015-2016).

Time: 2hrs Maximum Marks: 50. Section- a (soft skills). Answer any Two of the following 2x5=10. 1. What is positive thinking and its advantages? 2. How to improve a good body language? 3. What is SWOT/SWOC analysis and asses your SWOT analysis? 4. What is emotional intelligence and its importance? 5. Write a note on Netiquette and social media etiquette for college students. Section- b (paragraph writing). Answer any Two of the following 2x5=10. 1. What is a paragraph II Subject: Electronics Semester-III & IV Board of Studies - Electronics. 2 Gondwana University Gadchiroli Scheme of Bachelor of Science for Semester Examination Subject: Electronics Teaching Scheme Per Week Examination Scheme Class Semester Theory Paper Theory Total Periods Practical Theory Paper Theory Marks Internal Assessment Paper - I Paper - II Total Practical Marks Total Marks B. Sc. I II I I 6 + 1T* T* 6 I B. Sc. II III IV 6 + 2T* 6 I 6 + 2T* 6 I B. Sc. A 4 Details of the Syllabus Paper Second Year B.Sc. Subject: Electronics Scheme for Semester-III W.E.F No. of Periods per week (48 minutes each) Marks Lecture Practical Tutorial Theory Internal Assessment Practical Total P-1 P-2 T Paper I: Amplifiers Paper II: Digital Electronics - I 3. BSc Course Structure and Syllabus for all 3 Years. Bachelor of Science is a 3 Year Course where you will have two semesters each year. We also provided list of courses that follows in B.Sc. Program. Have a glimpse of the Topics and Subjects that occur in B.Sc 1st, 2nd and Final Year. Check for the B.SC Books and Notes Semester wise Details in PDF Format. Access all the study material and notes that are necessary during your B.Sc. Final Exam Preparation here. List of Bachelor Science Subjects. Physics. Zoology.