Structure of the Book

The Designer's Guide to VHDL is organized so that it can be read linearly from front to back. This path offers a graduated development, with each chapter building on ideas introduced in the preceding chapters. Each chapter introduces a number of related concepts or language facilities and illustrates each one with examples.

Scattered throughout the book are three case studies, which bring together preceding material in the form of extended worked examples. The previous editions of The Designer's Guide to VHDL were dedicated to my wife Katrina. As I said in the first edition preface, I used to think that authors dedicating their books to their partners was somewhat contrived, but that Katrina's understanding, encouragement and support taught me otherwise.

The System Designer's Guide to VHDL-AMS Analog, Mixed-Signal, and Mixed-Technology Modeling

Peter J. Ashenden
EDA CONSULTANT, ASHENDEN DESIGNS PTY. LTD.
VISITING RESEARCH FELLOW, ADELAIDE UNIVERSITY

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First comprehensive book on VHDL to incorporate all new features of VHDL-2008, the latest release of the VHDL standard; helps readers get up to speed quickly with new features of the new standard.

Presents a structured guide to the modeling f The System Designer's Guide to VHDL-AMS: Analog, Mixed-Signal, and Mixed-Technology Modeling by Peter J. Ashenden. Rating: 5 out of 5 stars (5/5). System Level Design with Rosetta by Perry Alexander. For the purposes of this book, we include any digital circuit that processes or stores information as a digital system. We thus consider both the system as a whole and the various parts from which it is constructed. Therefore, our discussions cover a range of systems from the low-level gates that make up the components to the top-level functional units.